

*10/10/10**11-13-62*

c Sub B crucial
a2 crucial
c crucial

a second router module having N2 signal input terminals, an expansion input terminal and (M2+1) output terminals, including M2 signal output terminals and an expansion output terminal, the second router module including a means for routing a signal received at any one of the N2 signal input terminals selectively to any one or more of the (M2+1) output terminals and for routing a signal received at the expansion input terminal selectively to any one or more of the M2 signal output terminals,

and wherein the expansion output terminal of the first router module is connected to the expansion input terminal of the second router module and the expansion output terminal of the second router module is connected to the expansion input terminal of the first router module.

Sub C crucial
a3

12. (Amended) A routing switch comprising n routers ($n > 1$) and wherein each router comprises p router modules ($p \geq 1$) each having a plurality of signal input terminals, a plurality of signal output terminals, (n-1) expansion input terminals and (n-1) expansion output terminals and including a means for routing a signal received at any one of its signal input terminals to any one or more of its output terminals and for routing a signal received at any one of the expansion input terminals to any one or more of the signal output terminals, and an expansion interconnect network whereby each expansion output terminal of the ith router module ($i = 1 \dots p$) of the jth router ($j = 1 \dots n$) is connected to an expansion input terminal of the ith router module of a router other than the jth router.

a4

Add new claim as follows:

13. (New) A routing switch according to claim 1, having (N_1+N_2) input terminals and (M_1+M_2) output terminals and comprising input interface circuitry connecting said (N_1+N_2) input terminals respectively to the N_1 signal input terminals of the first router module and the N_2 signal input terminals of the second router module, and output interface circuitry connecting the M_1 signal output terminals of the first router module and the M_2 signal output terminals of the second router module to said (M_1+M_2) output terminals respectively.